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PTO/SB/21 (08-00)

## TRANSMITTAL FORM

(to be used for all correspondence after initial filing)

Total Number of Pages in This Submission

Application Number 09/961,055

Filing Date September 24, 2001

First Named Inventor Masaaki HIROKI et al.

Group Art Unit 2673

Examiner Name Unassigned

Attorney Docket Number 0756-2367

### ENCLOSURES (check all that apply)

- Fee Transmittal Form
- Fee Attached
- Amendment / Reply
- After Final
- Affidavits/declaration(s)
- Extension of Time Request
- Express Abandonment Request
- Information Disclosure Statement
- Certified Copy of Priority Document(s)
- Response to Missing Parts/ Incomplete Application
- Response to Missing Parts under 37 CFR 1.52 or 1.53

- Assignment Papers (for an Application)
- Drawing(s)
- Declaration and Power of Attorney
- Licensing-related Papers
- Petition
- Petition to Convert to a Provisional Application
- Power of Attorney, Revocation Change of Correspondence Address
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- Request for Refund
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#### Remarks

The Commissioner is hereby authorized to charge any additional fees required or credit any overpayments to Deposit Account No. 56-2280 for the above identified docket number.

### SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT

Firm or Individual name Eric J. Robinson, Reg. No. 38,285  
Robinson Intellectual Property Law Office, P.C.  
PMB 955  
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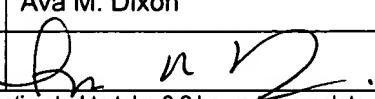
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Date 9-19-02

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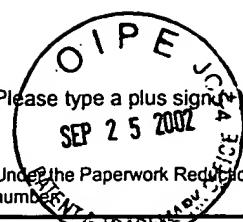
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<b>CHANGE OF CORRESPONDENCE ADDRESS <i>Application</i></b>  Address to: Commissioner for Patents Washington, D.C. 20231		Application Number	09/961,055
		Filing Date	September 24, 2001
		First Named Inventor	Masaaki HIROKI et al.
		Group Art Unit	2673
		Examiner Name	Unassigned
		Attorney Docket Number	0756-2367

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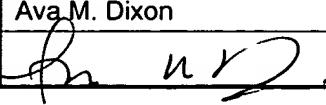
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In re Patent Application of

) Art Unit: 2673

Technology Center 2600

Masaaki HIROKI et al.

) Examiner: Unassigned

Serial No. 09/961,055

)

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**INFORMATION DISCLOSURE STATEMENT**

Commissioner for Patents

Washington, DC 20231

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and 37 C.F.R. 1.97-1.99, Applicant submits herewith a Form PTO-1449 listing references known to Applicant and requests that these references be considered by the Examiner and made of record in the above identified application. Copies of the references listed are submitted herewith in accordance with 37 C.F.R. 1.98(a).

In accordance with 37 C.F.R. § 1.97(e), it is certified that either (1) each item of information contained in the information disclosure statement except U.S. Patent 4,648,691 was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this information disclosure statement, or (2) no item of information contained in the information disclosure statement except U.S. Patent 4,648,691 was cited in a communication from a foreign patent office in a counterpart foreign patent application and no item of information contained in the information disclosure statement except U.S. Patent 4,648,691 was known to any individual designated in 37 C.F.R. § 1.56(c) more than three months prior to the filing of this information disclosure statement.

Specifically, the references not marked by an asterisk were cited against two counterpart Japanese patents, Japanese Patents 3,222,451 and 3,222,452, by an opponent during opposition. Both of these Japanese Patents issued in counterpart Japanese applications of the above-identified application.

The references marked with an asterisk were cited against a counterpart Japanese patent, Japanese Patent 3,222,446, by an opponent during the opposition period. This Japanese Patent issued in a counterpart Japanese Application of the above-identified application.

Applicants note that no English translation is available for the following references, therefore an explanation of each is as follows:

Color Liquid Crystal Display - This reference relates to an LCD having an integrated peripheral driver circuit using poly-Si TFT of N-type TFT and P-type TFT.

Flat Panel Display Device 1991 - This reference was cited for showing that it was well known to use an active matrix device for a view finder for a VTR having an integrated camera or other devices.

Flat Panel Display Device - 1992 - This reference appears to have been cited for showing that an active matrix liquid crystal display device has a driver circuit. Also, Fig. 16 shows a COG (chip on glass) for providing a driver LSI on a glass substrate of a liquid crystal panel. This reference appears to have been cited for showing that an active matrix liquid crystal display device has a driver circuit.

U.S. Patent 4,648,691 is in the family of JP 56-094386.

U.S. Patent 5,051,570, which was previously submitted, is in the family of JP 02-282728.

The Commissioner is hereby authorized to charge any fees connected with this filing which may be required now, or credit any overpayment to Deposit Account No. 50-2280.

Respectfully submitted,

  
Eric J. Robinson  
Reg. No. 38,285

Robinson Intellectual Property Law Office, P.C.  
PMB 955  
21010 Southbank Street  
Potomac Falls, Virginia 20165  
(571) 434-6789